

### ***Amendments to the Specification***

Please amend the specification as indicated.

Please amend paragraph [0082] as follows:

A method, according to another embodiment of the present invention, of phase-locking a transmit clock signal phase with a receive clock signal phase, is described in reference to FIGS. 22A and 22B. Method 2200 begins at step 2202. In step 2202, a predetermined phase difference and direction between a previous receive clock signal phase and a current receive clock signal phase is received. In step 2204, a current receive clock signal phase is received. In step 2206, the current receive clock signal ~~phase~~ phase is stored as a stored previous receive clock signal phase. In step 2208, a calculated phase difference and direction between the previous receive clock signal phase and the current receive clock signal phase is determined. In step 2210, a phase control selection signal is received. In step 2212, either the predetermined phase difference and direction or the calculated phase difference and direction is selected as the selected phase difference and direction to be used, depending on the phase control selection signal. In step 2214, a transmit phase lock signal is received. If the transmit phase lock signal is set, signifying that no adjustment is to be made, then the method continues at step 2216. In step 2216, the selected phase difference is changed to a value of zero. In step 2218, a previous transmit clock signal phase is received. In step 2220, the selected phase difference is added to or subtracted from (depending on the specified direction) the previous transmit clock signal phase to obtain an adjusted transmit clock signal phase. In this scenario, the transmit clock signal phase remains unchanged (i.e., no phase adjustment). Method 2200 terminates at step 2222.